L Number	Hits	Search Text	DB	Time stamp
1	158	logic same reconfiguration and dram	USPAT; US-PGPUB; DERWENT	2004/06/26 05:55
2	64	logic same reconfiguration and hdl	USPAT; US-PGPUB; DERWENT	2004/06/26 05:56
3	64	logic same reconfiguration and hdl and comp$6	USPAT; US-PGPUB; DERWENT	2004/06/26 05:59
4	56	logic same reconfiguration and hdl and compa$5	USPAT; US-PGPUB; DERWENT	2004/06/26 06:35
5	17	logic same reconfiguration and hdl and compa$5 and 716/$.ccls.	USPAT; US-PGPUB; DERWENT	2004/06/26 06:42
6	40	dram same fpga same logic and reconfiguration	USPAT; US-PGPUB; DERWENT	2004/06/26 07:17
7	40	memory same fpga same reconfiguration and comparator	USPAT; US-PGPUB; DERWENT	2004/06/26 07:22
8	2	memory same fpga same reconfiguration and comparator and hdl	USPAT; US-PGPUB; DERWENT	2004/06/26 07:18
9	52	memory same fpga and $2configuration and comparator and hdl	USPAT; US-PGPUB; DERWENT	2004/06/26 07:28
10	108	bist and hdl and memory	USPAT; US-PGPUB; DERWENT	2004/06/26 07:29
11	40	bist and hdl and memory and comparator	USPAT; US-PGPUB; DERWENT	2004/06/26 07:29
-	117	memory and hdl and address and switch and array and comparator and logic$2	USPAT; US-PGPUB; DERWENT	2004/06/25 08:19
-	177	self same configuration and memory and address and comparator and volatile	USPAT; US-PGPUB; DERWENT	2004/06/25 08:20
-	149	self same configuration and memory and address and comparator and volatile and array	USPAT; US-PGPUB; DERWENT	2004/06/25 08:43
-	4	self same configuration and memory and address and comparator and volatile and array and hdl	USPAT; US-PGPUB; DERWENT	2004/06/25 16:53
-	75	memory and address and comparator and volatile and array and hdl	USPAT; US-PGPUB; DERWENT	2004/06/26 05:51

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